## **REMARKS**

Claims 1-13 are present for examination.

Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Aratani et al. (U.S. Patent 6,538,675 B2) (hereinafter Aratani). Claims 7-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aratani. Claims 5-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aratani in view of Han (U.S. Patent 6,175,387 B1) (hereinafter Han). Claims 9-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over the admitted applicant's prior art (Figure 6) in view of Aratani and further in view of Han.

Applicant has amended each of the independent claims in order to more readily distinguish over the applied prior art. For example, claim 2 has been amended to recite an external memory device for storing decoded data of the first N-th image data generated by respective decoded devices and for storing filter parameters used for converting the image data into respective image formats. Further, claim 2 recites first to N-th image synchronization signal generating and synchronization adjusting devices for generating horizontal and vertical synchronized signals which are input together with the first to N-th image data converted respective predetermined image formats. Further, claim 2 recites wherein first to N-th vertical synchronized signals are generated synchronously in the same phase and wherein the filter parameters are output from the external memory portion to respective first to N-th image format conversion devices while no decoded data is output from the external memory device. Independent claim 9 has been amended to include a recitation that the filter parameters stored in the memory device are used for converting the image data into an image format and has further been amended to recite that the first and second vertical synchronization signals are generated synchronously in the same phase and the first and second filter parameters are output from the external memory device while no decoded data is output from the external memory device to be multiplexed with the first and second synchronization signals for outputting to respective first and second image format conversion devices. Claims 10, 12 and 13 have been similarly amended.

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It is submitted that none of the prior art discloses applicant's invention as now recited and as to the amended claims, and thus the Patent and Trademark Office has not made out a prima facie case of obviousness under the provisions of 35 U.S.C. 103.

All dependent claims are believed to be allowable for at least the same reasons as the independent claims from which they depend and based on the additional features that are recited in the claims themselves.

The application is now considered to be in condition for allowance and an early indication of same is earnestly solicited.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extensions fees to Deposit Account No. 19-0741.

Respectfully submitted,

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